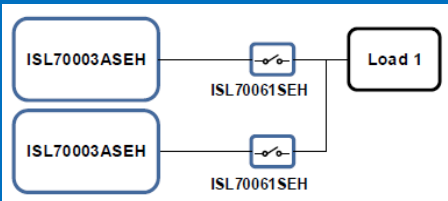


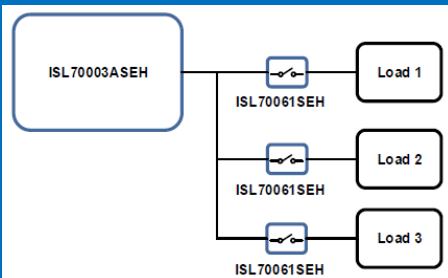
Applications

- Satellites power distribution management
- Power system redundancy
- Power sequencing
- Power system fault management
- Space VPX systems

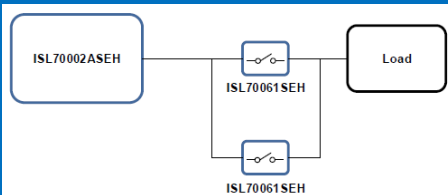
Redundant Source Switch:



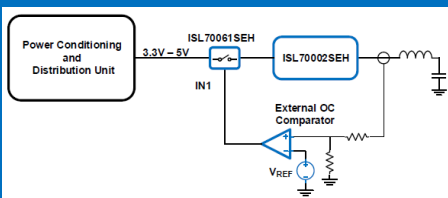
Redundant Load Applications:



Parallel Configuration to reduce Resistance or increase Current Capability:



Front-End Protection Switch:



Protec GmbH

Rosenheimer Landstraße 117
85521 Ottobrunn-Riemerling

eMail: sales@protec-semi.com

New Intersil Load / Protection Switches

Single Channel RadHard P- and N-MOS 10A Turn-off-time 3 μ s low R_{ON}

The **ISL73061SEH** and the **ISL70061SEH**, are radiation hardened single channel load switches featuring ultra-low r_{ON} and controlled rise time. These devices use a PMOS pass device as the main switch that operates across an input voltage range of 3V to 5.5V and can support a maximum of 10A continuous current. Simple ON/OFF digital control inputs make the device capable of interfacing directly with low voltage control signals from an FPGA, MCU, or processor.

Additional features include reverse current protection to stop current from flowing toward the input when the output SWO voltage increases above the input SWI voltage, a selectable 122 Ω MOSFET to discharge the output, and Undervoltage Lockout (UVLO) protection that keeps the switch OFF when the input voltage is too low.

RENESAS intersil Solutions for all your Missions		
Traditional Space	PEMs Plastic	Rad Tolerant Plastic
Hermetic Ceramic All Missions	10 years life time (MEO / GEO)	5 years life time (LEO)

The **ISL73062SEH** and the **ISL70062SEH**, are radiation hardened single channel load switches featuring ultra-low r_{ON} and controlled rise time. These devices use a NMOS pass device as the main switch that operates across an input voltage range of 0V to (VCC -2V) and can support a maximum of 10A continuous current. The devices have a VCC pin to power the logic and driver. The VCC voltage range is 3V to 5.5V. Simple ON/OFF digital control inputs make the device capable of interfacing directly with low voltage control signals from an FPGA, MCU, or processor.

Additional features include reverse current protection to stop current from flowing toward the input when the output SWO voltage increases above the input SWI voltage, a selectable 100 Ω MOSFET to discharge the output, and Undervoltage Lockout (UVLO) protection that keeps the switch OFF when the VCC voltage is too low.